

Abstract

A patch panel system including a chassis and a plurality of modules. The chassis includes elongated structures configured to interconnect top, bottom and side portions of the chassis. The elongated structures are also configured to receive and secure a printed circuit board and the plurality of modules to the chassis. The modules include a housing and a module card. The card can include a variety of connections that provide communication to connections located on a back plane of the chassis. The system can include a combination of passive and active modules that are interchangeable to provide a variety of interface configurations.

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